

* The small-signal ac analysis begins by removing the dc effects of V_{CC} and replacing the dc blocking capacitors C_1 and C_2 by short-circuit equivalents, resulting the network of Fig (5-20).

Z_i $Z_i = R_B // \beta r_e$ Ohms
 $\cong \beta r_e$ as R_B is greater than βr_e by more than a factor of 10

Z_o when $V_i = 0$, $I_i = I_b = 0$, o.c the current source

$Z_o = R_C // r_o \cong R_C$ if $r_o \geq 10 R_C$

A_v $V_o = -\beta I_b (R_C // r_o)$

$I_b = \frac{V_i}{\beta r_e}$

$V_o = -\beta \left(\frac{V_i}{\beta r_e} \right) (R_C // r_o)$

$A_v = \frac{V_o}{V_i} = - \frac{(R_C // r_o)}{r_e}$

$A_v = \frac{V_o}{V_i} = - \frac{R_C}{r_e}$ if $r_o \geq 10 R_C$

$A_i = \frac{I_o}{I_{in}} = \frac{V_o}{V_{in}} \times \frac{Z_{in}}{R_L}$
 $= A_v \times \frac{Z_{in}}{R_L}$

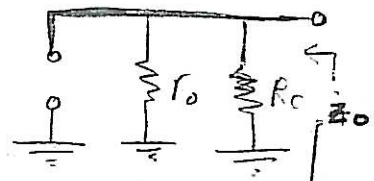


Fig (2-21) Determining Z_o for the network of Fig (5-20)

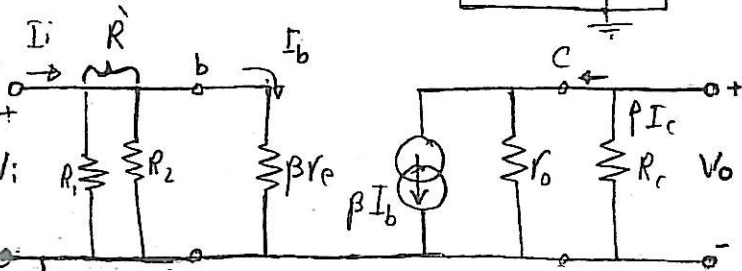
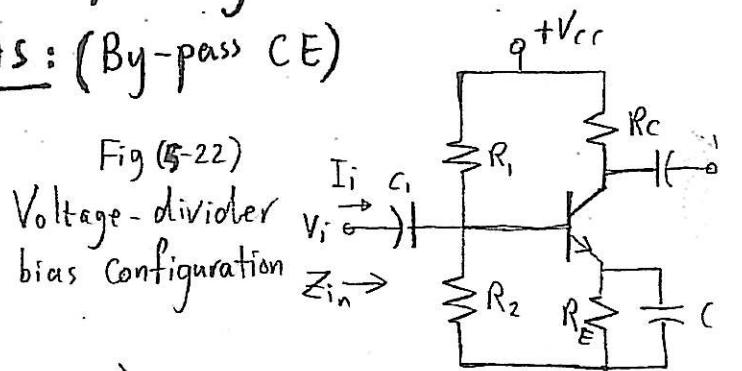


The negative sign reveals that a 180° phase shift occurs between the input and output signals.

7) VOLTAGE-DIVIDER BIAS: (By-pass C_E)

* The r_e model used to voltage-divider bias network.

* Note the absence of R_E due to the low-impedance shorting effect of the bypass capacitor, C_E .



$$\dot{R} = R_1 // R_2 = \frac{R_1 R_2}{R_1 + R_2}$$

$$\underline{Z}_i = \dot{R} // \beta r_e$$

Z_o with $V_i = 0$, $I_b = 0 \mu A$ and $\beta I_b = 0 mA$

$$Z_o = R_c // r_o$$

$$Z_o \approx R_c \quad \text{if } r_o \gg 10 R_c$$

$$\underline{A_v} \quad V_o = -(\beta I_b)(R_c // r_o)$$

$$I_b = \frac{V_i}{\beta r_e}$$

$$V_o = -\beta \left(\frac{V_i}{\beta r_e} \right) (R_c // r_o)$$

$$A_v = \frac{V_o}{V_i} = \frac{-R_c // r_o}{r_e}$$

$$A_v \approx \frac{V_o}{V_i} \approx -\frac{R_c}{r_e} \quad \text{if } r_o \gg 10 R_c$$

8) CE EMITTER-BIAS CONFIGURATION:

The r_e equivalent model is substituted in Fig (5-25), but note the absence of the resistance r_o . The effect of r_o is to make the analysis a great deal more complicated, and considering the fact that in most situations its effect can be ignored.

$$V_i = I_b \beta r_e + I_e R_E$$

$$= I_b \beta r_e + (\beta + 1) I_b R_E$$

$$Z_b = \frac{V_i}{I_b} = \beta r_e + (\beta + 1) R_E$$

$$Z_b \approx \beta (r_e + R_E) \quad \text{since } \beta \gg 1$$

$$Z_b \approx \beta R_E \quad \text{since } R_E \gg r_e$$

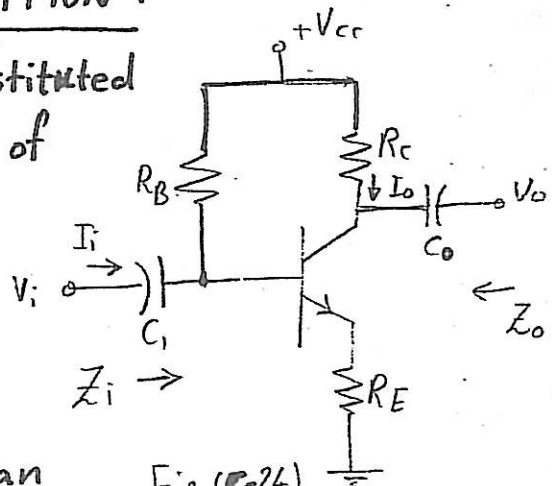


Fig (5-24)

CE emitter-bias Configuration

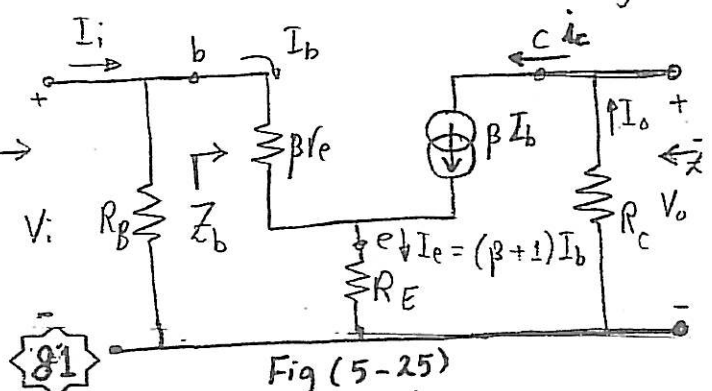


Fig (5-25)

$$\underline{Z}_i \parallel \quad Z_i = R_B \parallel Z_b$$

$$\underline{Z}_o \parallel \quad \text{set } V_i = 0, I_b = 0, \therefore \beta I_b = 0$$

$$Z_o = R_C$$

$$\underline{A}_v \quad I_b = \frac{V_i}{Z_b}$$

$$V_o = -I_o R_C = -\beta I_b R_C = -\beta \left(\frac{V_i}{Z_b} \right) R_C$$

$$A_v = \frac{V_o}{V_i} = - \frac{\beta R_C}{Z_b} = - \frac{-\beta R_C}{\beta (R_E + r_e)} = - \frac{R_C}{R_E + r_e}$$

$$A_v = \frac{V_o}{V_i} \approx - \frac{R_C}{R_E} \quad \text{as for approximation } Z_b \approx \beta R_E$$

Effect of r_o

After derivation using KVL, KCL, source conversion and Thévenin's theorem.

$$\underline{Z}_i \parallel \quad Z_b = \beta r_e + \left[\frac{(\beta + 1) + \frac{R_C}{r_o}}{1 + (R_C + R_E)/r_o} \right] R_E$$

since $\frac{R_C}{r_o} \ll (\beta + 1)$

$$Z_b \approx \beta r_e + \frac{(\beta + 1) R_E}{1 + (R_C + R_E)/r_o}$$

For $r_o \gg 10(R_C + R_E)$, $(\beta + 1) \approx \beta$

$$Z_b \approx \beta r_e + (\beta + 1) R_E \approx \beta (r_e + R_E)$$

$$\underline{Z}_o \parallel \quad Z_o = R_C \parallel \left[r_o + \frac{\beta (r_o + r_e)}{1 + \frac{\beta r_e}{R_E}} \right]$$

$r_o \gg r_e$

$$Z_o \approx R_C \parallel r_o \left[1 + \frac{1}{\frac{1}{\beta} + \frac{r_e}{R_E}} \right]$$

Typically $\frac{1}{\beta}$ and $\frac{r_e}{R_E}$ are less than one with a sum usually less than one. For $\beta = 100$, $r_e = 10 \Omega$ and $R_E = 1k\Omega$

$$\frac{1}{\frac{1}{\beta} + \frac{r_e}{R_E}} = \frac{1}{\frac{1}{100} + \frac{10}{1000}} = \frac{1}{0.02} = 50$$

$$Z_o = R_C \parallel 51 r_o \approx R_C$$



$$\underline{A_v} \quad A_v = \frac{V_o}{V_i} = \frac{-\beta R_c / Z_b \ll 1 + R_c / V_o}{1 + \frac{R_c}{V_o}}$$

$$A_v = \frac{V_o}{V_i} \approx \frac{-\frac{\beta R_c}{Z_b} + \frac{R_c}{V_o}}{1 + \frac{R_c}{V_o}}$$

For $V_o \gg 10R_c$

$$A_v = \frac{V_o}{V_i} \approx -\frac{\beta R_c}{Z_b}$$

g) EMITTER - FOLLOWER CONFIGURATION:

* When the o/p is taken from the emitter terminal of the transistor as shown in Fig (5-26). The output voltage is always slightly less than the input signal due to the drop from base to emitter, but the approximation $A_v \approx 1$, The fact that V_o "follows" the magnitude of V_i with an in-phase relationship accounts for the terminology emitter-follower

* The collector is grounded for ac analysis, it is actually a common-collector configuration, used for impedance matching.

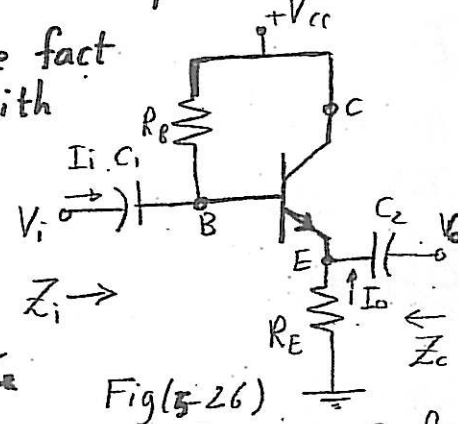


Fig (5-26)

Emitter-follower Config

$$\underline{Z_i} \quad Z_i = R_B // Z_b \quad (\text{high})$$

$$Z_b = \beta r_e + (\beta + 1) R_E$$

$$\approx \beta (r_e + R_E)$$

$$Z_b \approx \beta R_E \quad \text{for } R_E \gg r_e$$

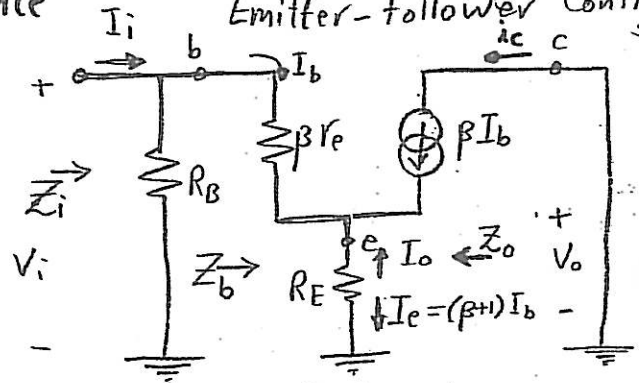


Fig (5-27)

r_e equ. cct. into the ac equivalent network of Fig (5-26)

$$\underline{Z_o} \quad I_b = \frac{V_i}{Z_b}$$

multiplying by $(\beta + 1)$ to establish I_e

$$I_e = (\beta + 1) I_b = (\beta + 1) \frac{V_i}{Z_b}$$

$$I_e = \frac{(\beta + 1) V_i}{\beta r_e + (\beta + 1) R_E} = \frac{V_i}{[\beta r_e / (\beta + 1)] + R_E}$$

$$I_e \approx \frac{V_i}{r_e + R_E}$$

but $(\beta + 1) \approx \beta$

$$\frac{\beta r_e}{\beta + 1} \approx \frac{\beta r_e}{\beta} = r_e$$