

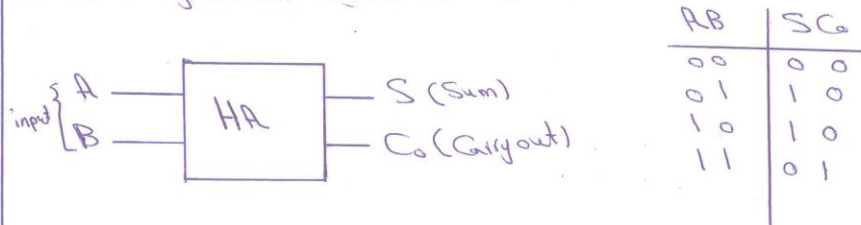
Adders:-

Adders are important not only in computers, but in many type of digital systems in which numerical data are processed. There are two types of adders:-

- 1- Half adder
- 2- Full adder

1- Half Adder (H.A) :-

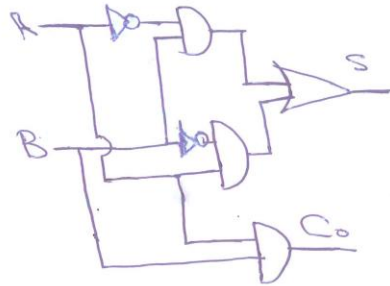
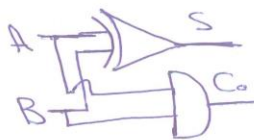
It is a digital circuit having two inputs and two outputs. The inputs are the inputs bits of the binary number and outputs are the sum of them and carry of them. The truth table and the block diagram are shown below:-



By means of using gates, we can have different forms of H.A.

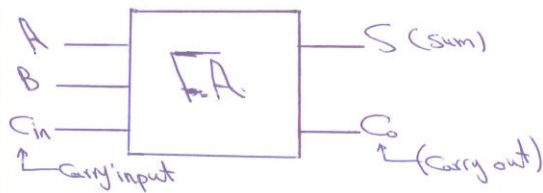
$$S = \bar{A}B + A\bar{B} = A \oplus B$$

$$C_o = AB$$



2- Full Adder (F.A)

Application for the H.A are limited. The H.A is no sufficient for adding more than two one-bit numbers. Full Adder is a logic circuit that can add 3-bit and have two outputs. These outputs are the sum of three bits inputs and the second output is Carry of them. The truth table and the block diagram are shown below:-



A	B	C _{in}	S	C _o
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

From the truth table:-

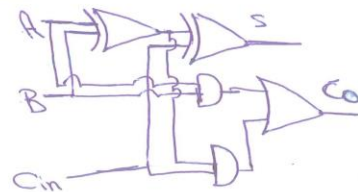
$$\begin{aligned}
 S &= \sum 1, 2, 4, 7 \\
 &= \bar{A}\bar{B}C_{in} + \bar{A}B\bar{C}_{in} + A\bar{B}\bar{C}_{in} + ABC_{in} \\
 &= \bar{A}(\bar{B}C_{in} + B\bar{C}_{in}) + A(\bar{B}\bar{C}_{in} + BC_{in}) \\
 &= \bar{A}(B \oplus C_{in}) + A(\bar{B} \oplus C_{in}) \\
 &= \bar{A}(B \oplus C_{in}) + A(B \oplus C_{in})
 \end{aligned}$$

$$B \oplus C_{in} = \overline{B \oplus C_{in}}$$

$$S = A \oplus B \oplus C_{in}$$

$$C_o = \sum 3, 5, 6, 7$$

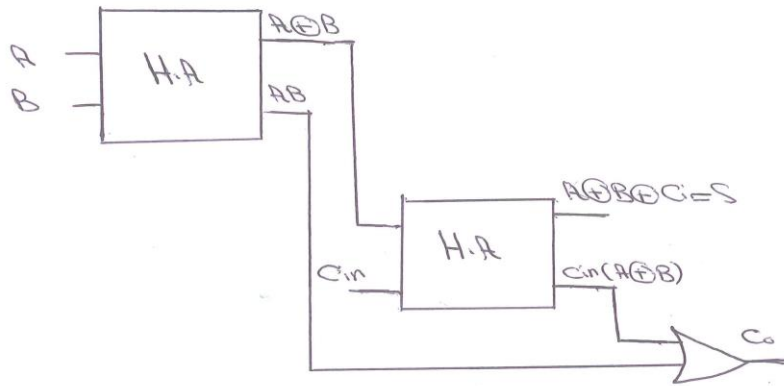
$$\begin{aligned}
 &= \bar{A}\bar{B}C_{in} + \bar{A}B\bar{C}_{in} + A\bar{B}\bar{C}_{in} + ABC_{in} \\
 &= AB(\bar{C}_{in} + C_{in}) + C_{in}(\bar{A}B + A\bar{B}) \\
 &= AB + C_{in}(A \oplus B)
 \end{aligned}$$



Ex:- Implement Full-Adder circuit diagram by using H.A blocks and any gate:-

Solution:- $S = A \oplus B \oplus C$
 $C_o = AB + C_i(A \oplus B)$] F.A \Rightarrow 3 bits
 الخرجات 3

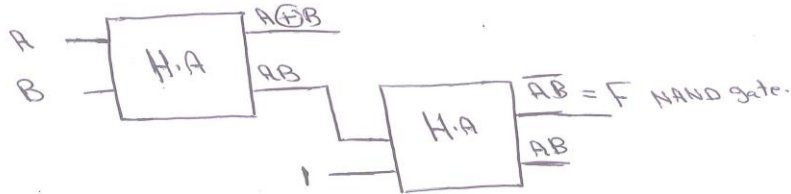
$S = A \oplus B$] H.A \Rightarrow 2 bits
 $C_o = AB$ الخرجات 2



Ex:- Design NAND gate by using H.A's only:-

Solution:- $F = \overline{AB}$] الخرجات 1
 الخرجات 1

$S = A \oplus B$] الخرجات 2
 $C_o = AB$ الخرجات 2

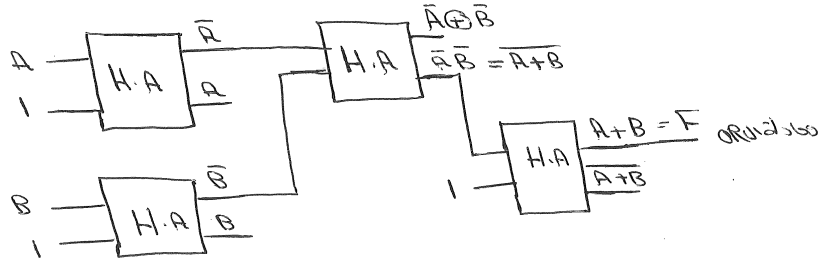


Note:- $A \oplus 1 = \overline{A}$
 $A \oplus 0 = A$] very Important.

Ex:- Design OR gate by using H.A only:-

Solution:- $F = A + B$] ^{الحل}
_{المطلوب}

$S = A \oplus B$] ^{المطلوب}
 $C_0 = AB$] _{المطلوب}



Ex:- Implement the following ^{boolean} functions below, using H.A blocks only:-

$X = A \oplus B \oplus C$ $Y = \bar{A}Bc + A\bar{B}c$ $Z = AB\bar{C} + C(\bar{A} + \bar{B})$
 $W = ABC$

Solution:- $X = A \oplus B \oplus C$
 $Y = \bar{A}Bc + A\bar{B}c = c(\bar{A}B + A\bar{B}) = c(A \oplus B)$] ^{المطلوب}
 $Z = AB\bar{C} + C(\bar{A} + \bar{B}) = AB\bar{C} + C(\bar{A} + \bar{B})$] _{المطلوب}
 $= AB \oplus C$

$W = ABC$
 $S = A \oplus B$] ^{المطلوب}
 $C_0 = AB$] _{المطلوب}

