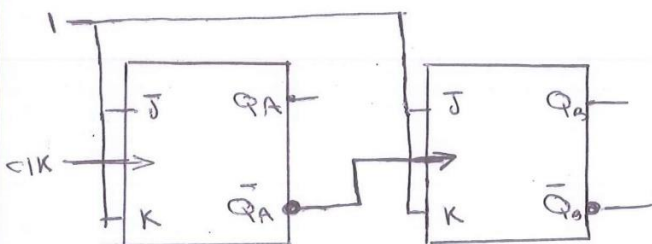
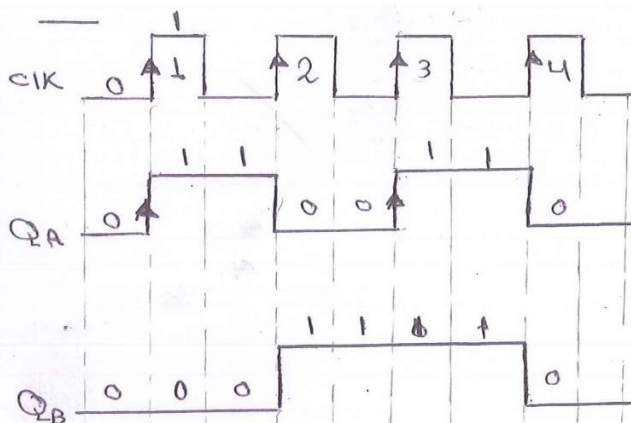


- Asynchronous (Ripple) Counters:-

Example:- 2 Bit Asynchronous Binary Counter:-

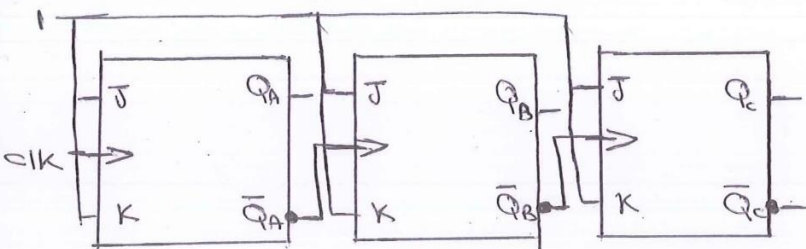


Note- 2-bit binary ripple counter consists of 2 (JK or T f.f). The f.f holding the LSB receives the incoming count pulse.



Clock pulse	QB	QA
Initially	0	0
1	0	1
2	1	0
3	1	1
4 (recycle)	0	0

Example:- Design Mod(8) Asyn Counter (8-state):-



Clock pulse	QC	QB	QA
Initially	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
4	1	0	0
5	1	0	1
6	1	1	0
7	1	1	1
8 (recycle)	0	0	0

