

Demultiplexing the Bus $AD_7 - AD_0$

- The high order address is placed on the address bus and hold for 3 clk periods,
- The low order address is lost after the first clk period, this address needs to be hold however we need to use latch
- The address $AD_7 - AD_0$ is connected as inputs to the latch 74LS373.
- The ALE signal is connected to the enable (G) pin of the latch and the OC – Output control – of the latch is grounded

The Overall Picture

- Putting all of the concepts together, we get:

